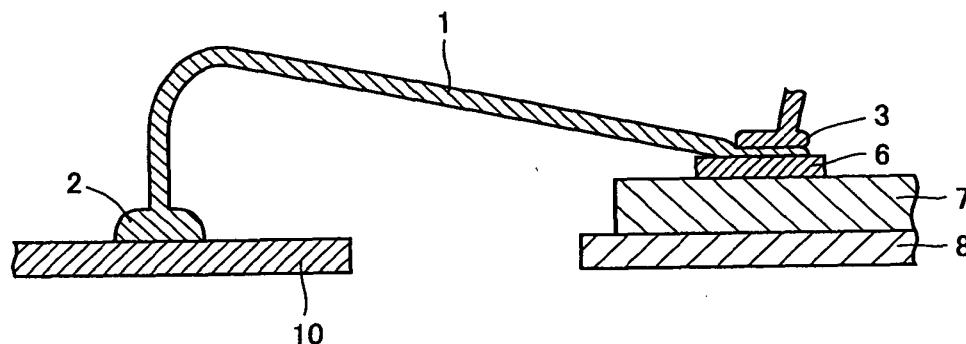


APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
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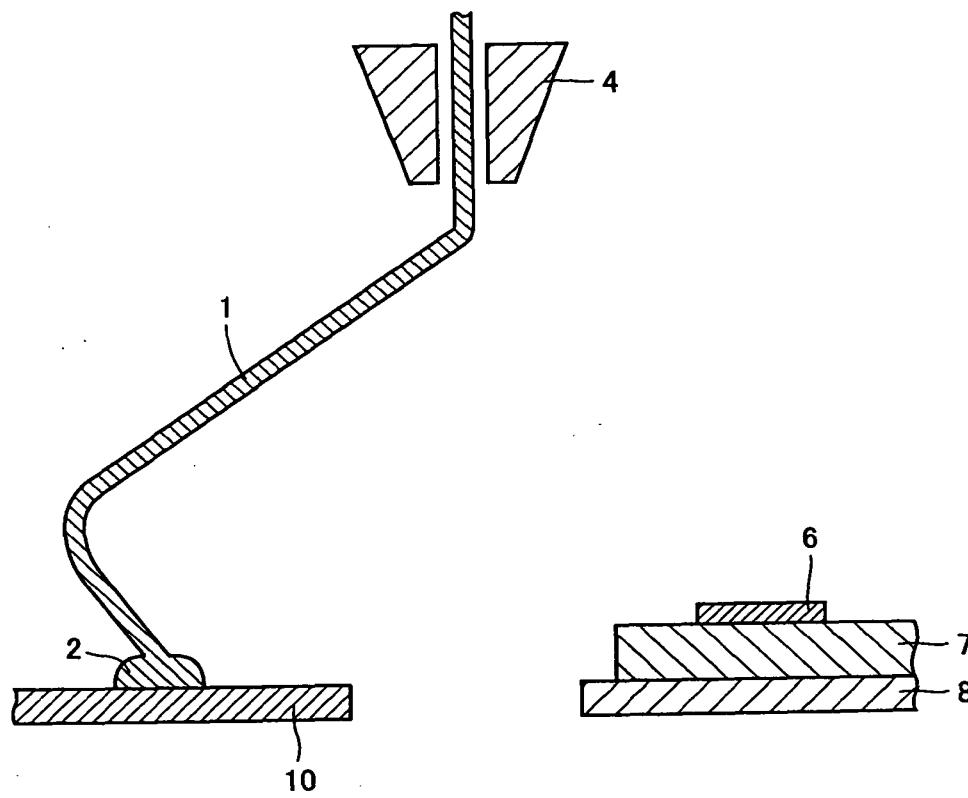
Title: Semiconductor Device and Manufacturing Method Thereof
Inventors: Hideyuki ARAKAWA
Atty Docket No.: 401346
Leydig, Voit & Mayer, Ltd. 202-737-6770

FIG.1



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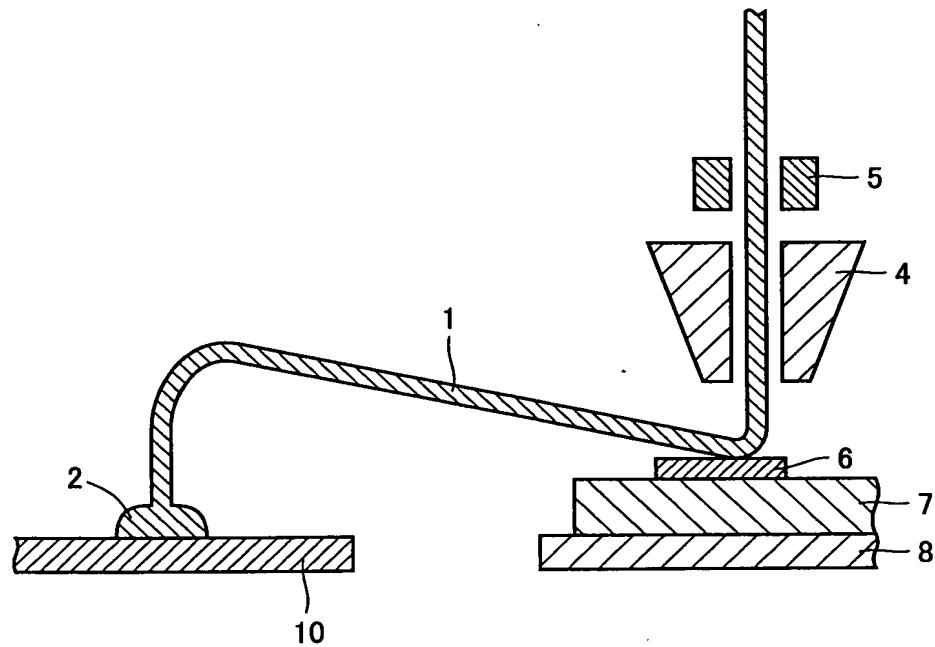
FIG.2



APPROVED	O.G. FIG.
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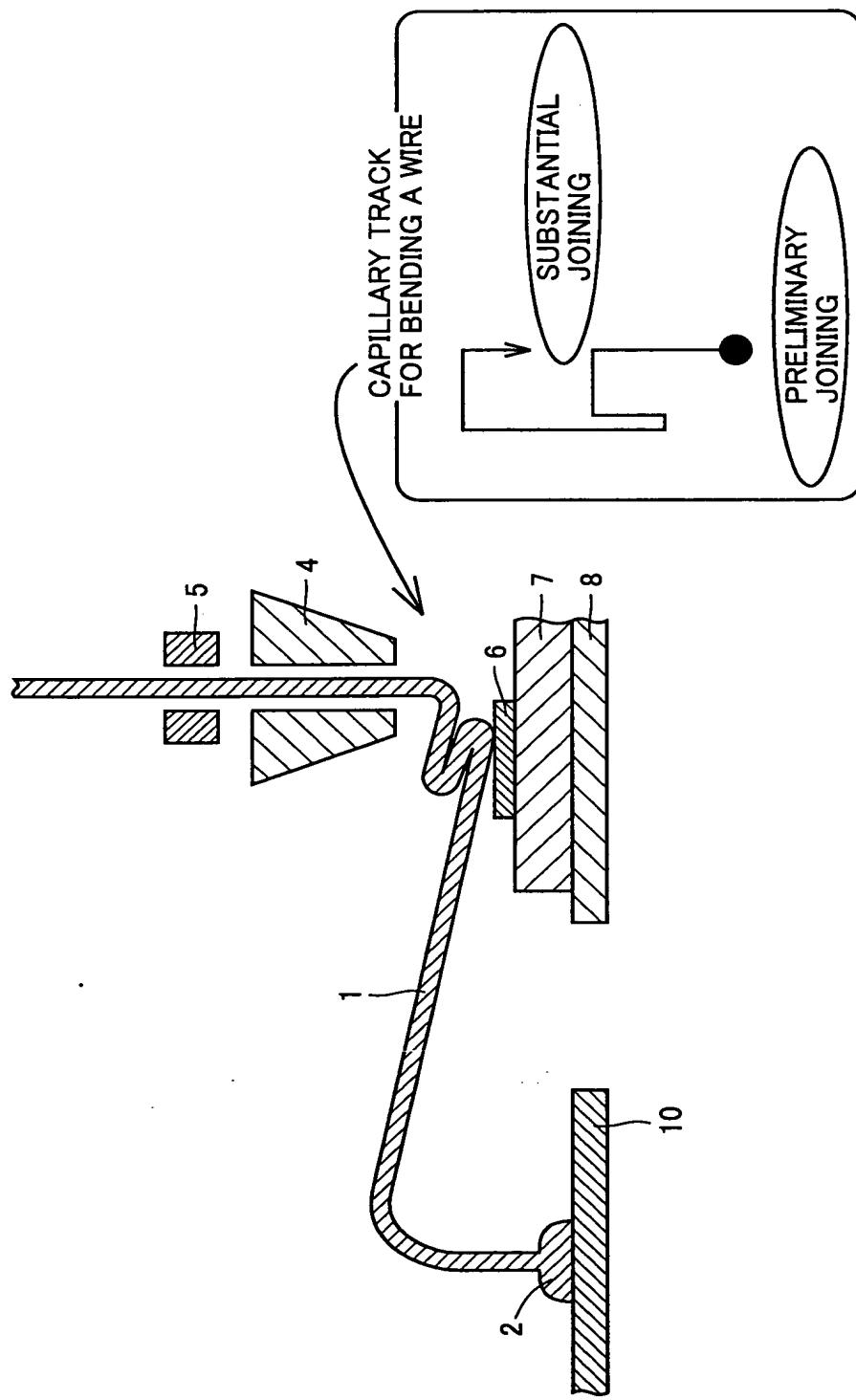
Title: Semiconductor Device and Manufacturing Method Thereof
Inventors: Hideyuki ARAKAWA
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FIG.3



APPROVED	O.G. FIG.	
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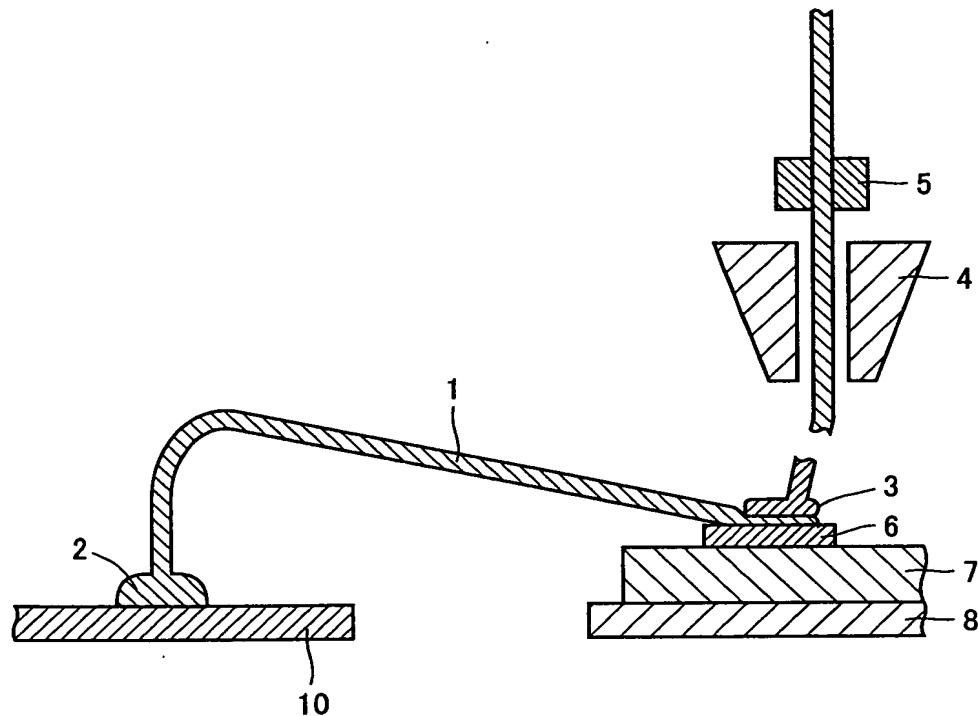
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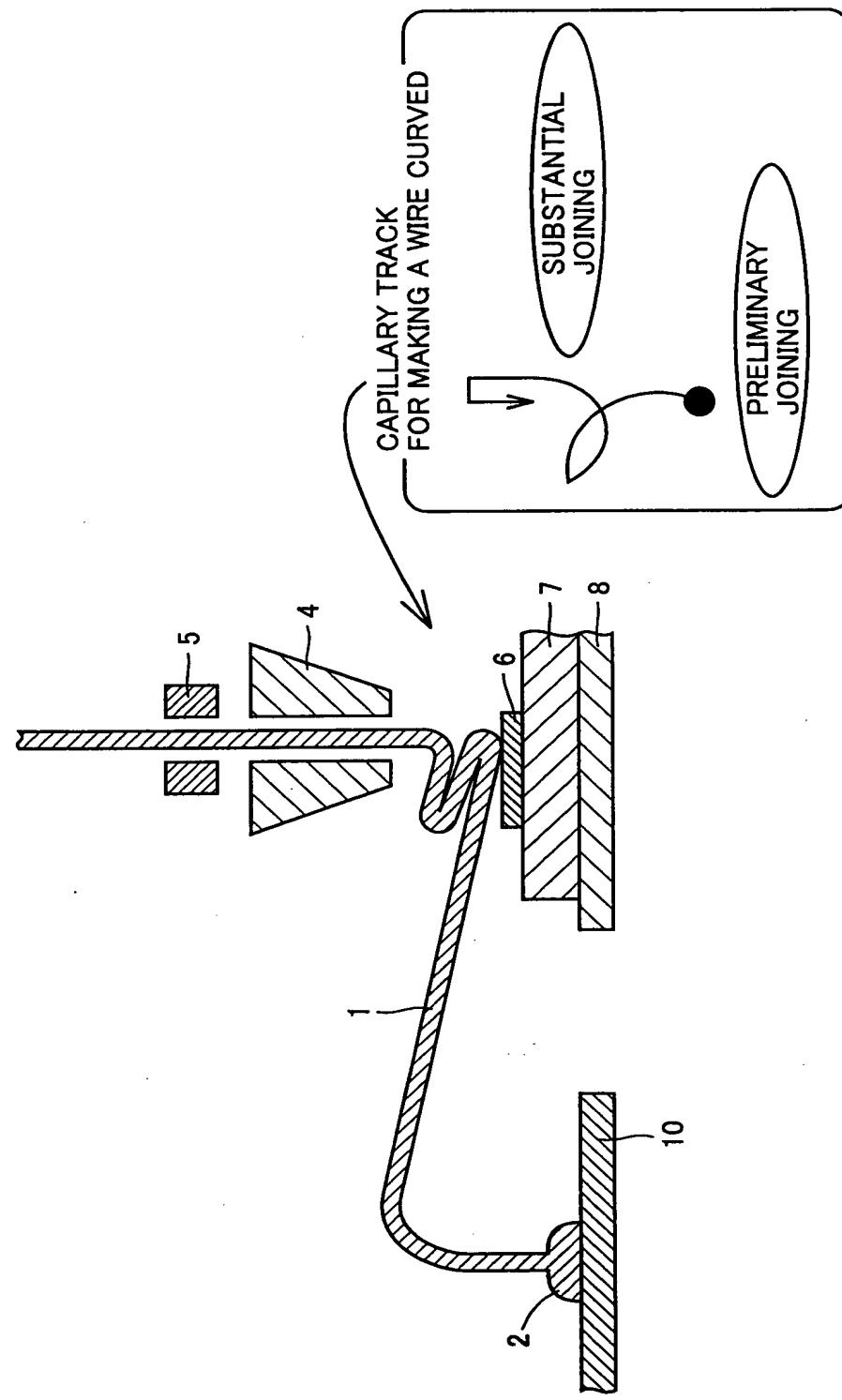
FIG.5



100-330-344350

APPROVED	O.G. FIG.
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FIG. 7A

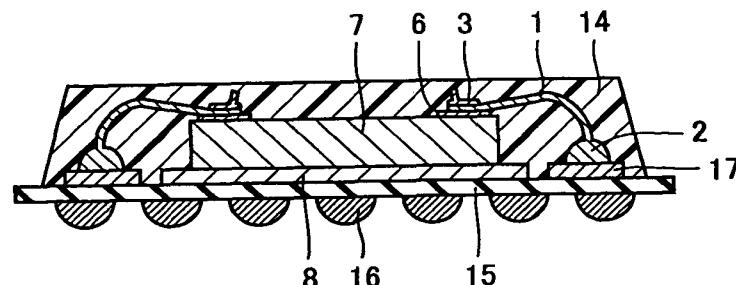


FIG. 7B

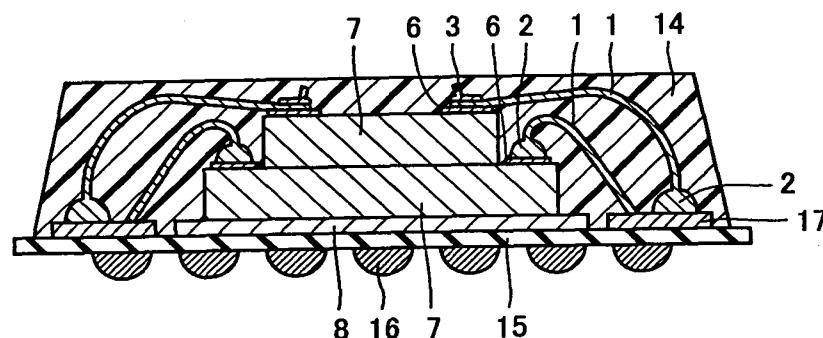
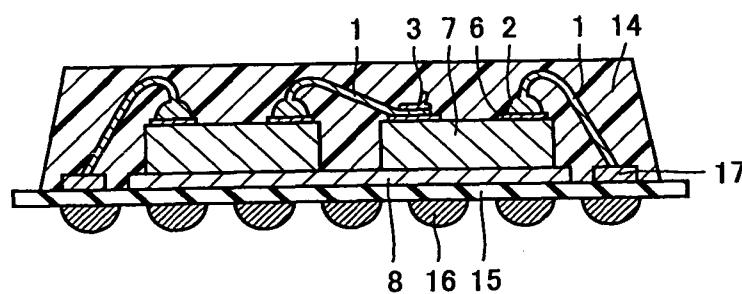


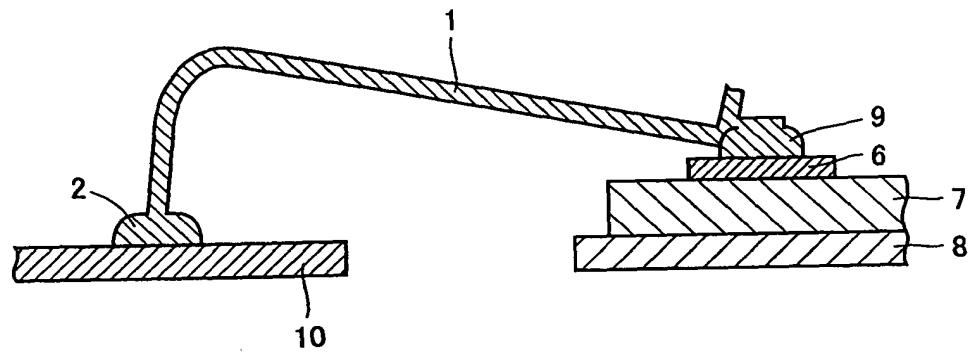
FIG. 7C



APPROVED BY	O.G. FIG. CLASS	SUBCLASS
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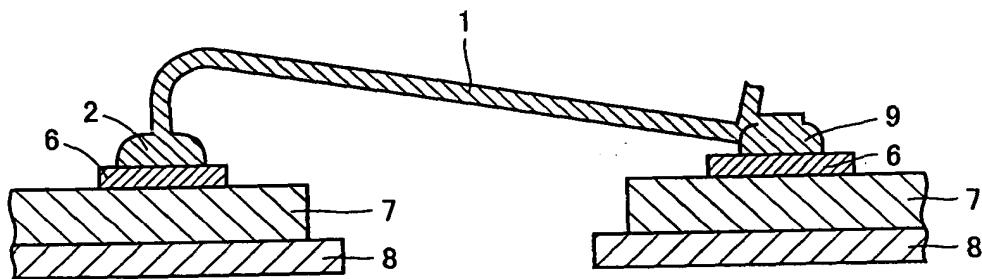
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Inventors: Hideyuki ARAKAWA
Atty Docket No.: 401346
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FIG.8 PRIOR ART



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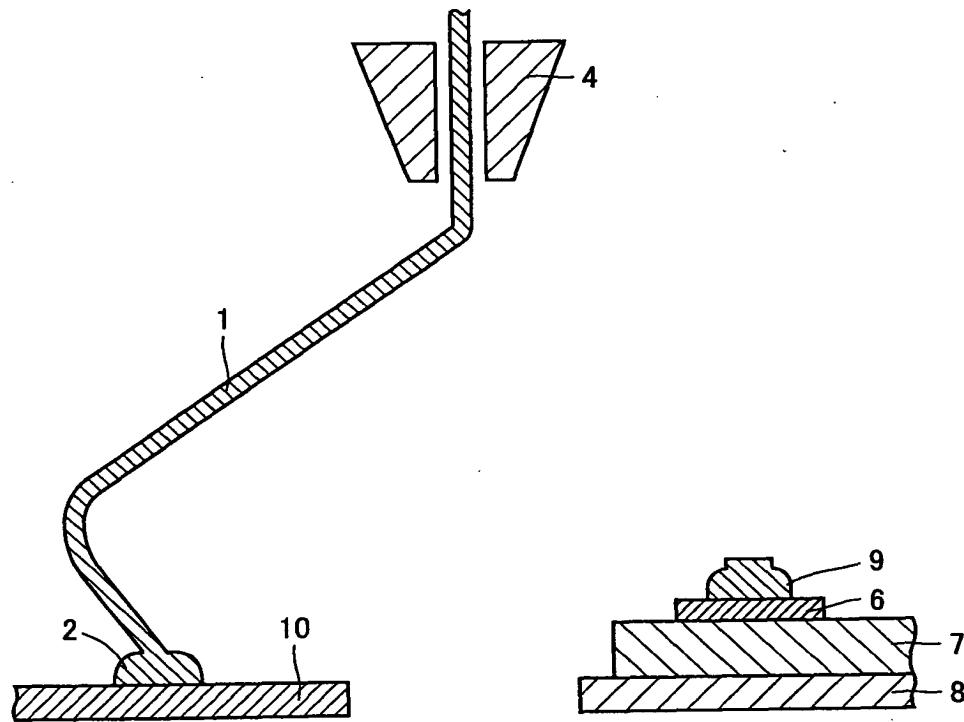
FIG.9 PRIOR ART



APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

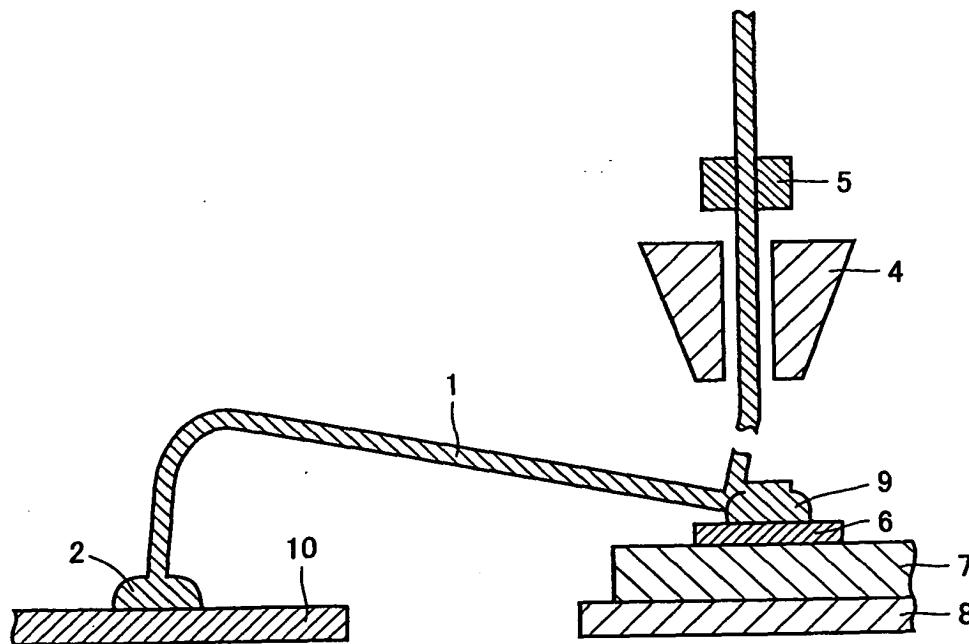
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FIG.10 PRIOR ART



06-09161-3432300

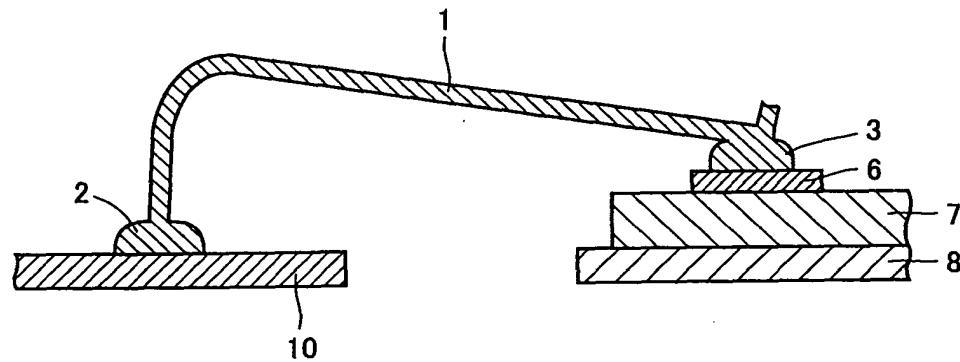
FIG.11 PRIOR ART



APPROVED	O.G. FIG.
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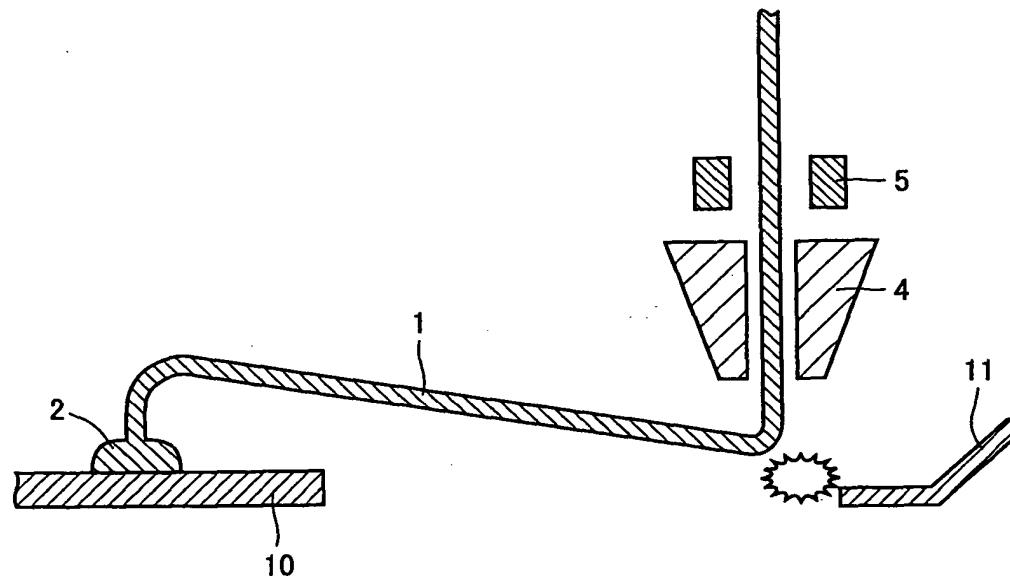
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FIG.12 PRIOR ART



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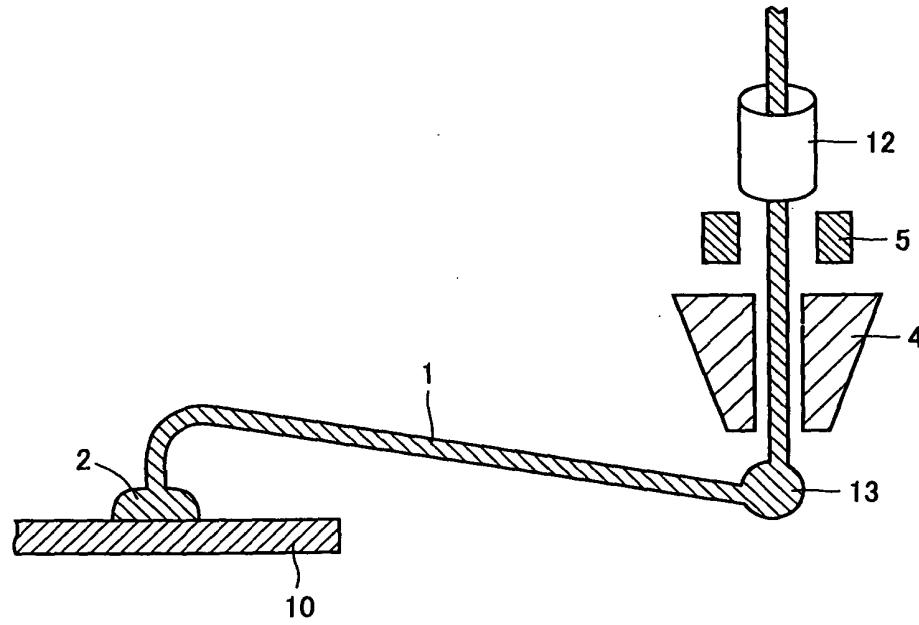
FIG.13 PRIOR ART



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FIG.14 PRIOR ART



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FIG.15 PRIOR ART

